

Supports the following ARM®
Processor/Cores:

ARM9E, ARM11, Freescale i.MX, TI,
Cortex-M1, Cortex-M3,
Cortex-R4 and
Cortex-A8, A9, A15
Cortex-A53, A57

Key Benefits

• **Powerful** - The Arium LC-500Se offers unparalleled run control functionality and execution processing for ARM-architecture processors.

• **Integrated & Intuitive** - The Arium LC-500Se, provides integrated run control and Coresight Trace support when paired with the SourcePoint™ debugger. This highly integrated, intuitive environment designed to help you minimize iterative steps and shorten your debug cycle.

• **User-friendly** - The Arium LC-500Se is easy to install and configure, giving you a fast start to your debug project.

• **Flexible** - The Arium LC-500Se works with a number of ARM-architecture cores/processors.

• **Value Packed** - Full ARM and Thumb™ instruction set debug at a competitive price.

• **Fully Supported** - All Arium run-controlled probes produced for the embedded market are fully supported by our highly trained staff of applications engineers.

Arium LC-500Se Run-Control Probe

The Arium LC-500Se is our most popular probe for targets with ARM® cores. The unit offers reliable run control and intuitive stepping features. It's designed for speed, too, with fast file and image downloads and quick stepping through code. When coupled with SourcePoint™, ASSET's flagship debugging software, the hardware assisted debugger operates on Microsoft® Windows® and Linux hosts and offers debug of Linux kernel, device driver, and application source code. Featuring real-time event management and independent processor control, the Arium LC-500Se delivers superb visibility to and manipulation of source code. More information on SourcePoint can be found on the web site under www.asset-intertech.com/Products/SourcePoint/SourcePoint-for-ARM.



Specifications:

Environmental

32-90° F (0-31° C)
Maximum humidity - 85%

Communications

10/100/Ge Ethernet
USB 2.0

Device Frequency
Up to 20 MHz

Download Speed

>200 KBytes/sec

Trig In

>50 Ohm, 155 ns maximum delay

Trig Out

50 Ohm, 125 ns maximum delay

Dimensions (inches)

(W) 3.800
(L) 5.004
(H) 1.510



The Arium LC-500Se uses an ARM-powered® processor for robust performance and throughput.

PC Host Requirements

- Intel/AMD processor (2.4 GHz recommended)
- Microsoft Windows Vista/7 or Linux
- SVGA monitor (1024 x 768 or higher)
- 120 MB hard disk space
- 2G RAM (8G recommended)
- 10/100/Ge Base-T Ethernet or USB 2.0
- or-
- USB 1.1 / 2.0 (USB 2.0 recommended)

Supported processors:

ARM RISC core architecture

- ARM9E™
- ARM11™
- Cortex™ ARM
- Cortex™ A53/A57

Freescale architecture

- i.MX6
- i.MX 21
- Control plane processors
- Network processors

Texas Instruments processors

- Stellaris™, Sitara™
- OMAP™, Hercules™

