QUICK START GUIDE

USING PFX ON A ZYNQ-7000 ZEDBOARD



Table of Contents

Introduction	. 3
Software Setup	3
Hardware Setup	. 3
Examples in Development Mode	3
Putting it all together – Manufacturing Mode	20
Learn More	22

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Introduction

This guide is intended to take the user through the steps to needed run PFx (Processor-based Functional Test or Processor-based Fast Programming) tests on a Zynq-7000 ZedBoard.

Software Setup

The computer needs one of the following products installed and licensed:

- ScanWorks® Processor-based Fast Programming for ARM 32-bit Processors
- ScanWorks® Processor-based Functional Test for ARM 32-bit Processors
- ScanWorks® Processor-based Functinal Test DDR for ARM 32-bit Processors

Hardware Setup

A computer is connected to a hub or switch via an Ethernet cable. A ScanWorks Remote Instrumentation Controller (RIC-1000) is connected to the hub or switch via an Ethernet cable. The Ethernet port on the ZedBoard is connected to the hub or switch via an Ethernet cable. The RIC-1000 is connected to the ZedBoard's JTAG port via a RIC-1000 UUT cable and an adapter for the Zynq JTAG port.

Examples in Development Mode

- 1. Start ScanWorks.
- 2. Click on the Import Button.









Click on Browse and browse to the file to be imported –
 C:\ScanWorks\Examples\PFx\ZedBoard Example Project.zip and clcik on Open.

ScanWorks	
Import a Project	Help G Back
1. Select the Import 2. Specify Name 3. Import File 3. Import 3. Import	
Import File Browse Select a compressed ScanWorks Project	
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ScanWorks			- X
Import a Project 1. Select the Import File 2.	Specify Name 3. Import		Help 🛛 Back
Import As Link Import to Source	Zedboard_Example_Project Forbidden Characters: / ? \ * : " > < .		
Destination Directory	C:\ScanWorks\Projects	Browse	
		Previous	

4. Click on the Next Button.

5. Click on either Import Button.

ScanWorks		
Import a Project		Help 🛛 🕒 Back
1. Select the Import File	2. Specify Name 3. Import	
Zip File Source	C:\Scanworks\Examples\PFx\Zedboard_Example_Project.zip	
Project Name will be	Zedboard_Example_Project	
Link to Source?	false	
Import to	C:\ScanWorks\Projects	
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	Previous	







Project: Zedboard Example Project		No Hardwa	ire		퍜: Options	Licenses	? Help
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Action Name	🔺 Туре 🔶						
Run DDR3 Test and Tune	Processor Functional Test DDR						
Run Device Presence Tests - Ethernet Phy Flash+MMC Card	+Spi Processor Functional Test						
Run Ethernet Loopback Tests	Processor Functional Test						
Run MMC Programming using Ethernet	Processor Fast Programming						
Run QSPI and MMC over JTAG+QSPI over MMC	r Processor Fast Programming						
Run QSPI Programming using Ethernet	Processor Fast Programming	•					
							ScanWo

6. Click on *No Hardware* to select the hardware controller.

7. Select RIC-1000 as the controller.









8. Configure the RIC-1000's ip address, and select a TCK Frequency of 32 MHz. Note that flash programming performance over JTAG is directly affected by the TCK frequency. The higher the TCK Frequency, the better the programming performance. Click OK.

Setup RIC-10	00 Hardware	ОК
TCK Frequency (MHz):	Delay Compensation:	Cancel Help
TAP Voltage:	IP Address: 192.168.12.119	
Discrete IO TAP Vol	Itages	
3.3 Discete IO Signal 2 and 3	• •	
Discrete IO Signal 4 and 9	5: •	
Discrete IO Signal 6 and 3.3	7:	

9. Now let's run the DDR3 Test and Tune Action from within Edit mode.









10. Right Click on DDR3 Test and Tune. This will launch the Development interface.



11. Click on the Connect Button. This will download a target agent to the ZedBoard and begin communicating with the target agent.

VTOS Developer™ [3.0.14.2] (DDR3 Test and Tune)				X
File Tools View Licensing				Help
Add Connect Run Disconnect				
Welcome!			CONNECT DDR TFTP	
Project	Settings Console			
Project DDR3 Test and Tune Project DDR3 Test and Tune Control Test and Tune Test and Test and Tune Test and Test and Tune Add Test and Test and Tune Test and Test and Tune Test and Tes	General Name Processor Stop On Failure Normalize Files Target Load Firmware on Connect Issue Target Version Issue Reset on Disconnect VTOS Runner	DDR3 Test and Tune Zynq-7000 Z-7020 True True False False False		
Task	Loging Option Use Timestamps Run Option Run Delay (ms) Loop Option Loop Stop Condition Name Name of the selected item	Append False Stop on Failure 0 Loop Once 1		
Copy Delete Up Down				
Project: C:\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\DDR3	Test and Tune\DDR3 Test and Tune\DDR3 Test and Tune.project (Zynq	-7000 Z-7020) [VTOS DDR] Elapsed: 00	00:00:00:00	







12. You can manually run any of the tests in the DDR Interactive Tests group or the DDR Tune Task by right clicking on it and selecting Run. Click on the Run Button near the top of the window to run the test(s) that are in the DDR Exported Tests group.

VTOS Developer ³⁴ [3.0.14.2] (DDR3 Test and Tune)			-	• X
File Tools View Licensing				Help
Add Connect Run Disconnect				
Done		CONNECT	DDR TFTP	
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Project: C:\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\DDR3 Te	est and Tune\DDR3 Test and Tune\DDR3 Test and Tune.project (Zynq-7000 Z-7020) [VTOS DDR] Elapsed: 00	0:00:14:903		

13. Add a test to the DDR Exported Tests group by right clicking on *Add Test>* beneath DDR Comprehensive Test. Select DDR Performance Test from the list of tests. Then hit File->Save to save the updated project. Hit the Run button at the top of the interface.









14. Close the Development window, and return to the ScanWorks Actions view. Click on the Run Button to the left of DDR3 Test and Tune to run the DDR Exported Tests as a ScanWorks Action.

ScanWorks: Zedboard_Example_Project.Zedboard		
Project: Zedboard_Example_Project Design: Zedboard		FRIC-1000 Image: Options Image: Licenses ? Help O Exit TCK Frequency: 32.000 MHz IP Address: 192.168.12.119 Compatible with Design? Yes
Projects Designs Actions Sequen	ices	Status Notes Mappings Reports Passed
	Type	 -> 0:100 resp='' > Test passed; status=100 test.ddr.performance // DDR Performance Test - Access Time SDRAM Performance Suite SDRAM: cache read (32-bit) [00000000_00000000 - 00000000_000ffff] READS : average seek = 0.000 000 000, throughput = 412.914 Mi8/s SDRAM: cache write (32-bit) [0000000_00000000 - 00000000_000ffff] WRITES: average seek = 0.000 000 000; throughput = 905.989 Mi8/s Test Suite Totals: 2 Passed, 0 Failed, 0 Aborted, Running time 1.629 secs -> 0:100 resp='' -> Test passed; status=100
Run Device Presence Tests - Ethernet Phy+Spi Flash+MMC Card	Processor Functional Test	Done. Done! Action passed!
Run MMC Programming using Ethernet	Processor Fast Programming	Run time: 20.701
Run OSPI and MMC over ITAG+OSPI over	Processor Fast	• ScanWork

15. Right Click on Device Presence Tests – Ethernet Phy+Spi Flash+MMC Card to enter the Development interface for the Device Presence Tests.

Tools View Licensing		He
Add Connect Run Disconnect		
lcomel		CONNECT PIN TFTP
ect	Settings Console	
Project:Device Presence Tests - Ethernet Phy-Spi Flash-MMC Card ☺ Connections Settings □ Connection Settings ○ Condection Settings ○ Add Card Card Settings ○ add Card Card Settings ○ Add Card Card Settings ○ Tasks □ □ Tasks	General Name Processor Stop On Failure Normalize Files	Device Presence Tests - Ethernet Phy-Spi Flash - MMC Card 2ynq-7000 2-7020 True True
G <add task=""></add>	Target Load Firmware on Connect Issue Target Version Issue Reset on Disconnect VTOR Sumper	False False False
k (* Configuration [VT0S Scan Plus] (* Board Malaization (* Board Malaization (* Configuration (* Configurat	Logging Option Use Timestamps Run Option Run Delay (ms) Loop Option Loop Stop Condition	Append Faite Stop on Failure O Loop Once 1
	Name Name of the selected item	
iopy Delete Up Down	<u> </u>	







16. This action checks for the presence of the Ethernet Phy, The flash device on the QSPI port, and the presence of the MMC device. Hit the Connect button, then the Run button.



17. If the SD/MMC Verify test fails, the most likely cause is the SD/MMC card in your ZedBoard is a different make/model than the one used when creating this guide. To update the settings for your SD Card, click on SD/MMC Verify test and then change the Manufacturer ID, OEM ID and size fields to match your SD card. Note that the size of the SD/MMC card is printed out in decimal in the test, but the input field is expecting a hexadecimal entry. Save the Project. Hit the Run button to verify the change in settings have corrected the problem.

VTOS Daveloner ^{av} (2014.2) /Davise Brasense Tests - Ethernet Dhu-Sni Elech-	MMC Card)	
File Tools View Licensing	mme caro)	Hain
		Telp
Add Connect Run Disconnect		
Done		CONNECT PIN TFTP
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L⊕ ≪4dd Fadure> ⊖ %g Tasks Tasks	Actions Setup Actions Cleanup Actions	<add actions=""> <add actions=""></add></add>
• «Add Task»	Specific Manufacturer Id	0x02
	OEM Id Capacity	0x544D 0x0075C000
Task		
● ● Ehemed Group' ● ● Phre ● ● Phre ● ● Phre ● ● Chemed First trad ● ● Chemed Trade ● ● SPR Group ● ● SPR		
	Manufacturer Id The 8-bit expected manufacturer ID returned by the SD/MMC device i	in the Card Identification register (CID).
Copy Delete Up Down		
Project: C:\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\Device P	Presence Tests - Ethernet Phy+Spi Flash+MMC Card\Device Presence	e Tests - Ethernet Phy+Spi Flash+ Elapsed: 000:00:05:061







18. Close the Development window, and return to the ScanWorks Actions view. Click on the Run Button to the left of Device Presence Tests – Ethernet Phy+Spi Flash+MMC Card to run the Device Presence Tests as a ScanWorks Action.



19. Right click on the Ethernet Loopback Tests action to open the Development interface for the Ethernet Loopback Tests.

• VTOS Developer™ [3.0.14.2] (Ethernet Loopback Tests)			
File Tools View Licensing		Help	
Add Connect Run Disconnect			
Welcome!		CONNECT PIN TFTP	
Project	Settings Console		
Task Configuration (VTOS Scan Ethemed Configuration Conf		Ethamat Loophak Tasts 20m8 7000 27000 True False False False Step on failure 0 Loop Once 1	
Copy Delete Up Down			
Project: C\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\Ehernet.LoopbackTests\Ehernet.LoopbackTests\EhernetLoopbackTests.project. (2)rq=7000 Z-7020) [V]			







20. This action tests for the presence of the Ethernet Phy, does loopback tests on the ethernet MAC and loopback tests on the ethernet phy. Click on Connect and then on Run.



21. Close the Development window, and return to the ScanWorks Actions view. Click on the Run Button to the left of Ethernet Loopback Tests to run the Ethernet Loopback Tests as a ScanWorks Action.

ScanWorks: Zedboard_Example_Project.Zedboard		
Project: Zedboard_Example_Project Design: Zedboard Projects Designs Actions Sequer	ces	✓ RIC-1000 Image: Options ? Help © Exit TCK Frequency: 32.000 MHz IP Address: 192.168.12.119 Compatible with Design? Yes Status Notes Mappings Reports Passed
✓Create ✓ Manage Search: Find Actions		▲ Loopback test completed successfully, received packet count 64 → 0:100 resp='' → Test passed; status=100 0 #ENET.LB.PHY 1000 FALSE 64 enet.loopback.test // Ethernet PHY Loop back Test - 1000 Mbo(0, 1000, 64)
Action Name	Туре 👳	Ethernet Packet Test: PHY loopback, 1000 Mbps
Run DDR3 Test and Tune	Processor Functional Test DDR	gem0: initializing physical interface (PMY0) timeout (5000 ms) waiting for link to come up on PMY0
Run Device Presence Tests - Ethernet Phy+Spi Flash+MMC Card	Processor Functional Test	Loopback test completed successfully, received packet count 64
Run Ethermet Loopback Tests Image: Copy Copy Run > Rename Image: Copy Copy Image: Reports Requirements	Processor Functional Test	-> 0:100 resp='' -> Test passed; status=100 Done. Done! Action passed!
Run MMC Programming using Ethernet	Processor Fast Programming	Run time: 25.787
		ScanWorks*







22. Right click on QSPI Programming using Ethernet to open Development interface for the QSPI using Ethernet action. This action tests for the presence of a flash memory device on the QSPI port, Erases the flash, does a blank check on the flash, Programs the flash with a test file over Ethernet, and then tests the checksum of the range of flash memory that was programmed.

VTOS Developer [™] [3.0.14.2] (QSPI Programming using Ethernet)		
File Tools View Licensing		Help
Add Connect Run Disconnect		
Welcome!		CONNECT PIN TETP FLASH
Project	Settings Console	
Project dSR Programming using Element Constant Relings Cons		could be this computer or another network computer. The default list a provided based on IP Addresses for this computer. Invalid values
Copy Delete Up Down		
Pariate Colombiants Pariate 7 alternal Controls Pariate 7 alternal OCDI Pariate		in a second s

23. To use the ethernet port on the UUT as the data pipe, the ethernet on the UUT must be configured, and a TFTP client must be configured. Click on TFTP Server under the Task tab to configure the UUT's ipaddress and the TFTP client settings. Clicking on the pull down to the right side of TFTP Server Address will show a list of available addresses to use. Use the ipaddress of your computer as the ipaddress of the TFTP Server.

♦ VTOS Developer [™] [3.0.14.2] (QSPI Programming using Ethernet)		
File Tools View Licensing		Help
Add Connect Run Disconnect		
Welcome!		CONNECT PIN TFTP FLASH
Project	Settings Console	· · · · ·
□ ① Project CSP Programming using Ethemet □ ① Consciences Setings □ ○ Add/Tadara □ ○ Add/Tadara □ ○ Add/Tadara □ ○ Add/Tadara	General Name Processor Stop On Falure Normaliae Files Target Load: Firmare on Contect Issue Target Vector Issue Relet on Discontect VOOR Revenue	059 Programming using Ethemet 2/mp 7000 2-7020 True True False False
Task	Logging Certion Logging Certion Run Obly (mi) Loop Option Loop Step Condition	Append False Stop on Falure 0 Loop Once 1
Copy Delete Up Down		
Project: C:\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\QSPI Project	ogramming using Ethernet\QSPI Programming using Ethernet\QSPI Program	ming using Ethernet.proj Elapsed: 000:00:00:000







24. Now configure the ipaddress of the UUT by entering the desired value in the field to the right of Target IP Address. The appropriate subnet mask will be calculated and entered automatically. Save the project.

ile Tools View Licensing		
Add Connect Run Disconnect		
FTP Server was successfully stopped.		CONNECT FIN TFTP FLASH
rojact	Settings Console	
El] Pojoci dDP Pograminis poligi Ellemot El] Constanti Sellemot El] Constanti Sellemot I Constanti Sellemot Constanti Sellemot El Tasta Poli Call Tasta Di Call Tasta Constanti Sellemot Constanti S	Canner Name Action Action Setup Actions Clearup Actions Clearup Actions Clearup Actions Clearup Actions Clearup Actions Target DPI Actions Target MPA Adutes Target MPA Adutes	vadd Actions - vadd Actions - Teac ROMM ROMM (Robound Gyade MID) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
aak	Target IP Address	192.168.12.204
b € Configuration (7703 Perginan BPI Dannet) b € Configuration (7703 Perginan BPI Dannet) b ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕ ⊕	1170 Server IP Address 4 Gateway Use a Gateway Target Gateway IP Address	382 348 32 238 Feine 38 18 38 3
Constant Constan	Target IP Address The IPv4 Address used by the target direct board for network	communications, using dot notation. Invalid values will not be saved.
Conv Delete Un Down		
copy Delete Op Down		

25. Now that the TFTP Server is configured, The TFTP Server needs to be started. From the top memu bar of the Development interface, Use the Tools pulldown and select Tools-> Start TFTP Server. Note that the TFTP Server continues to run until it stopped via Tools->Stop TFTP Server or the computer is shut down. You can tell the TFTP Server is running when TFTP is shown in green in the upper right hand corner of the Development Interface.









- With Standard (1000 Hogonaning using Electron)
 With Lending
 Hogonaning using Electron(1000 Figure 100 Fi
- 26. Click on Connect and then click on Run.

27. In the example above, the flash memory was programmed at a rate of 749 KiB/second. Close the Development interface, and click on the Run button to the left of QSPI Programming using Ethernet to run the action from ScanWorks.

ScanWorks: Zedboard_Example_Project.Zedboard		
Project: Zedboard_Example_Project Design: Zedboard		FRC-1000 E Options Licenses ? Help Ø Brit TCK Frequency: 32.000 MHz IP Address: 192.168.12.119 Compatible with Design? Ves Ves
Projects Designs Actions Sequer	nces	Status Notes Mappings Reports Passed
✓ Create ✓ Manage Ø Search: Find Actions	5	 Programmed from TFTP 0x000000000_001000000 bytes, in 1.350 364 708 sec onds, throughput of 758.313 KiB/s -> 0:100 resp="'
Action Name	Type	-> Test passed; status=100 s' QSPI' 0x0000000000000000:64 0x0000000000000000:64 s' aa26ff5b8953
Run DDR3 Test and Tune	Processor Functional Test DDR	S6bcffd9292ba9f89e66' TRUE image64.md5.test // Image Checksum Test(QSP I) QSPI: MDSSUM Verify Test: start offset 0x0 0x0, byte count 0x0 0x100000 could locate in the count of the count 0x0 0x100000
Phy+Spi Flash+MMC Card	Processor Functional Test	D6AIC6= A2N1 mm2=995011208A22200C110373709A18A600
Run Ethernet Loopback Tests	Processor Functional Test	-> 0:100 resp=''
Run MMC Programming using Ethernet	Processor Fast Programming	-> rest passed; statussien Done. Done!
Run QSPI and MMC over JTAG+QSPI over MMC	Processor Fast Programming	Action passed!
Run QSPI Programming using Ethernet Edit Copy	December 5 at	Run time: 28.657
	Prococcor Fact	ScanWorks*







28. Right click on the MMC Programming using Ethernet to open the Development interface for the MMC Programming action.

◆ VTOS Developer [™] [3.0.14.2] (MMC Programming using Ethernet)		
File Tools View Licensing		Help
Add Connect Run Disconnect		
Welcome!		CONNECT PIN TFTP FLASH
Project	Settings Console	
Image: Solution of the second sec	Central Name Processor Stop On Falve Normalize Files Target Los d'Immune on Connect Issue Target Version Issue Kest on Disconnect · VTOS Mune Logging Option Use Timetanpis Run Day (ms) Loop Option Loop Stop Condition Name of the selected item	MMC Programming uting Ethernet 2mg-7000 27000 True Faite Faite Faite Faite Site on Faiture O Loop Once 3
Copy Delete Up Down		
Project: C:\ScanWorks\Projects\Zedboard_Example_Project\Zedboard\MMC	Programming using Ethernet\MMC Programming using Ethernet	et\MMC Programming using Ethernet.pr Elapsed: 000:00:00:000

29. This action tests for the presence of the SD/MMC card, erases a portion, performs a blank check on the portion, programs the SD/MMC card with a file using the ethernet as the data pipe, and runs a checksum on the portion programmed. Configure the TFTP ipaddresses as shown in the QSPI Programming using Ethernet TFTP Server setup. Save the Project. Note that the TFTP Server does not have to be started again. Click on Connect and Run.







30. Note that in the example above, the SD/MMC card was programmed at a rate of 1.03 MiB/second. Close the Development injterface, and click on the Run button to the left of MMC Programming using Ethernet.

ScanWorks: Zedboard_Example_Project.Zedboard		
Project: Zedboard_Example_Project Design: Zedboard		RIC-1000 Image: Comparison of the second
Projects Designs Actions Sequer	ices	Status Notes Mappings Reports Passed
Create ✓ Manage	3	 onds, throughput of 976.011 K18/s > 0:100 resp-"' -> Test passed; status-100 s' SDWWC 1:1' 0x00000000000060:64 s' az26ff5
Action Name	Туре 🔶	b895356bcffd9292ba9f89e66' TRUE image64.md5.test // Image Checksum Tes
Run DDR3 Test and Tune	Processor Functional Test DDR	t(SDMMC 1:1) SDMMC 1:1: MDSSUM Verify Test: start offset 0x0 0x800000000, byte count 0x0 0x100000
Run Device Presence Tests - Ethernet Phy+Spi Flash+MMC Card	Processor Functional Test	Device='SDMMC 1:1' md5=aa26ff5b895356bcffd9292ba9f89e66
Run Ethernet Loopback Tests	Processor Functional Test	-> 0:100 resp=''
Run MMC Programming using Ethernet Edit $& 20 \text{ Copy}$ Run $> \text{Rename}$ Delete $m \text{ Reports}$ Requirements	Processor Fast Programming	-> Test passed; status=100 Done. Done! Action passed! Run time: 22.23
Run OSPI and MMC over ITAG+OSPI over	Processor Fast	ScanWorks*

31. Right click on the QSPI and MMC over JTAG+MMC over QSPI to open the Development interface for the programming action.

♦ VTOS Developer [™] [3.0.14.2] (QSPI and MMC over JTAG+QSPI over MMC)		
File Tools View Licensing		Help
Add Connect Run Disconnect		
Welcome!		CONNECT PIN TFTP FLASH
Project	Settings Console	
☐ Project OSP1 and MMC over JTAG-QSP1 over MMC ☐ © Connection Seetings ☐ France[1] ④ < ddd Fature> 월 Task: VTOS Program SPI MMC ④ < ddd Task>		QSPI and MMC over JTAG-QSPI over MMC 2ynq-7000 2-7020 True True False False False
Task Configuration [VTOS Program SPI IMMC] Image Board Board Instantion Board Instantion Scripts Scrips Scrips	Loging Option Use Timestamps Run Option Run Delay (ms) Loop Option Loop Stop Condition	Append Faite Stop on Failure 0 Loop Once 1
Image Checksum Test SP (Tash Image Enase Test SD Card Image Bank Test SD Card Image Bank Test SD Card Image Bank Test SD Card Image Theotexim Test SD Card Image Theotexim Test SD Card Image Test SD Test SD Test SD Card Image Test SD Test SD Test SD Card Image Test SD Test		







32. This action shows other capabilities of PFx. In this action, the Flash on the QSPI is tested for presence, the presence of the SD/MMC card is tested, the Flash is erased, a blank check is run on the flash, the flash is programmed over the JTAG port (useful if your UUT does not have an ethernet port) and the checksum test is run. The SD/MMC card is erased, blank checked, programmed via the JTAG port and the checksum is tested. After that is an example of programming the SD/MMC card using the flash on the QSPI port as the data source (device to device programming). Click on Connect and then Run.



33. In the example above, the flash on the QSPI is programmed over JTAG at a rate of 81.1 KiB/second, the SD/MMC card is programmed over JTAG at a rate of 97.9 KiB/second, and the SD/MMC card is programmed from the Flash device on the QSPI at a rate of 1.44 MiB/second. Close the Development interface. This concludes the Debelopment portion of the User's Guide.







Putting it all together – Manufacturing Mode

When all the development is complete and it is time to use PFx on a manufacturing line, it is desired to run all the actions with a single click. The mechanism for doing this is Sequencing. To run all the actions as a sequence:

1. In ScanWorks, click on the Sequences tab.

ScanWorks: Zedboard_Example_Project.Zedboard	
Project: Zedboard_Example_Project Design: Zedboard	RIC-1000 ≇Options Licenses ? Help O txit TCK Frequency: 32.000 MHz IP Address: 192.168.12.119 Compatible with Design? Ves
Projects Designs Actions Sequences	Status Notes Reports
+ Create • Auto-Create	ScanWorks loaded. *** License Check Out *** Checked out: PFPT_ARM_ML_SUB Checked out: PFPT_32ARM_DLSUB Checked out: PFT_DDR_32ARM_DLSUB Checked out: PFT_DDR_32ARM_DLSUB Project Zedboard Example_Project loaded Loading Zedboard Please wait Loading of design Zedboard complete
	ScanWorks*

2. Click on the Auto-Create button.

ScanWorks: Zedboard	d_Example_Project.Zedboard					×
Project: Zedbo Design: Zedbo Projects De	oard_Example_Project oard esigns Actions Sequences		FIC-10 TCK Freque IP Address Compatible Status	000 ency: 32.000 : 192.168.12 e with Desi Notes	북 Options 오니cer MHz 119 gn? Yes Reports	1ses ? Help O bot
+ Create	Auto-Create A Edit Manage	Precondition	Project	Design	Sequence	
Mapping Default	Reports 6 Purge Run 23 Serial Number Enter Serial Number	Loop 1 Status		es]		
Enable Rows	Sequence Steps	Status				
0	SPV1					
0	MMC Programming using Ethernet					
•	QSPI and MMC over JTAG+QSPI over MMC					
0	QSPI Programming using Ethernet					
	DDD2 Test and Tune		•			ScanWorks*







3. Hitting the Run button causes all of the actions to be executed.

Abort apping Serial Number Status Default Enter Serial Number Status Enable Sequence Steps Status e SPV1 Passe e SPV1 Passe e QSPI and MMC over JTAG+QSPI over MMC Passe e QSPI Programming using Ethernet Rum e QSPI Programming using Ethernet Rum e DDR3 Test and Tune Passe e DDR3 Test and Tune Passe e Device Presence Tests - Ethernet Phy+Spi Flash+MMC Card Passe e Ethernet Loopback Tests Image anWorks: Zedboard_Example_Project anWorks: Zedboard_Example_Project esign: Zedboard Projects Designs Actions Sequences e Careets Pedit Manage
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