

## *Raising the Bar with JTAG Innovations*



International Test  
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Three Time Winner!



## *JTAG Innovations*

### New Introductions

- PCI-200EJ Controller for structural JTAG test and functional emulation test
- New ScanWorks® release with increased openness

### Innovation Demonstrations

- Internal JTAG (IJTAG)
- Remote System Test (SJTAG)



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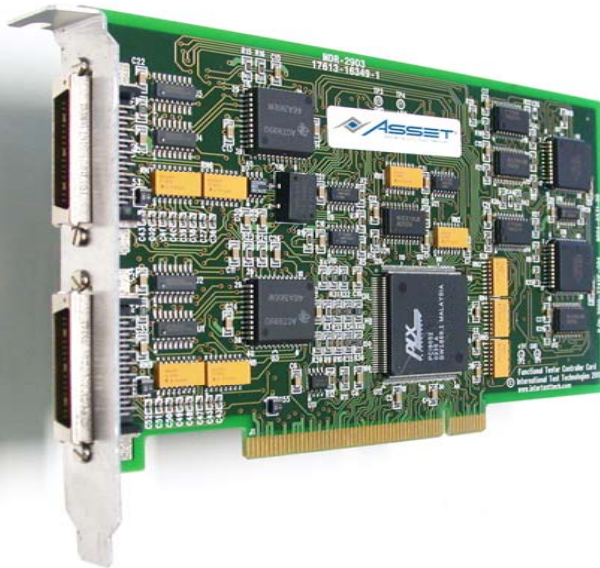
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## PCI-200EJ Controller

- Increased test coverage from one platform
- First JTAG controller to perform both structural JTAG and processor-based functional emulation tests
- Streamlines the entire test process
- Developed in collaboration with strategic partner, International Test Technologies
- PowerPC emulation supported now with Pentium® and XScale® emulation 4Q2006



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## ***ScanWorks®: The Open JTAG System***

**Increased openness to third-party technologies and products**

### **External I/O Management**

- Automates and provides GUI for incorporating third-party hardware for testing external I/O signals
  - AccessExtender™ family from JEK-Tech
  - ASSET's BSIO 400
  - Easy integration of custom-designed modules

### **SigmaQuest**

- Gather information across multiple geographies, test technologies and the supply chain
- Consolidate business intelligence across product design, manufacturing, supply chain and repair/return operations

## *ScanWorks®: The Open JTAG System*

### ScanWorks' History of Openness

- Agilent Medalist 3070 and i500 ICT
- Teradyne ICT
- ITT's  $\mu$ Master processor emulation
- InterComm Expert™ from PTC
- Embedded JTAG controllers from National, TI and Firecron
- National Instruments LabVIEW, LabWindows, and TestStand
- IEEE 1532 ISC engines from Xilinx and Lattice

### *Openness Means Power for ScanWorks Users*

## *Innovation Demonstrations*

### Internal JTAG (IJTAG)

- IEEE P1687 Draft Standard for Access and Control of Instrumentation Embedded Within a Semiconductor Device
- On-chip control and access methods
- For testing, characterizing and validating the performance of semiconductor devices

### System-Level JTAG (SJTAG)

- SJTAG study group led by major telecom OEMs
- Firecron embedded JTAG controller
- ScanWorks vectors applied remotely over an Ethernet WAN/LAN link

## *Other Recent ASSET Innovations*

- DFT Analyzer™ – The first JTAG DFT Tool
- The only JTAG tool supporting Intel® IBIST
- **FREE** Syntactic and Fee-Based Silicon BSDL Validation Services
- First to test high-speed buses with 1149.6



## *Innovations on the Horizon*

- Design Validation
- System-level JTAG
- Chip-level JTAG (IJTAG)

