

White paper: Carrier Grade Linux: High Service Availability for the Next-Generation Network

Know the differences in highly available operating systems â€” how it impacts total cost and reliability of network equipment. This paper provides a concrete description of...

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LGA1366 interposer

With ASSET
InterTech's new
LGA1366
interposer, design,
test and field repair
engineers are able to access the debug port on the
Intel Xeon 5500 Series and Core i7 processors



access the debug port on the Intel Xeon Processor 5500 Series and Core i7 processors

based on Intel's new Nehalem microarchitecture, after the processors have been placed on a circuit board

More cost-effective non-intrusive board test technologies such as processor-controlled test (PCT) on ASSET's ScanWorks platform for embedded instrumentation

Lower cost-of-test, non-intrusive board test (NBT) also offers improved test coverage over the older intrusive test technologies such as in-circuit test (ICT)

ASSET's LGA1366 interposer provides a direct interface between the CPU on an assembled circuit board and PCT on the ScanWorks platform

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