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Al Crouch, chief technologist, ASSET Core Instrumentation.

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ASSET Develops Tools Ahead of IJTAG Standard

(April 29, 2008) RICHARDSON, Texas Anticipating ratification of a final internal JTAG (IJTAG) standard, ASSET InterTech Inc. is developing and will bring to market open embedded instrumentation tools based on the preliminary IEEE standard, P1687 IJTAG.

"A great deal of validation and test instrumentation technology is being embedded into silicon these days," said Crouch, adding that, to take advantage of this trend, the industry requires an open environment that can access all of this embedded instrumentation technology to organize it, schedule its execution, access data collected by the instruments, analyze this data, display results, and exert overall control. "The IJTAG standard, once it is ratified, will provide the foundation for doing this."

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ASSET also added two technologists to lead its development of IJTAG tools. Al Crouch, formerly chief scientist and director of DfX research and development at Inovys Corp. and Verigy Ltd., became chief technologist, core instrumentation. Crouch has served for the last three years as vice chairman of the P1687 IJTAG working group that is developing the IJTAG standard and contributed to the hardware architecture definition. Over the last 20 years, he worked in design for test (DfT) at Freescale Semiconductor (formerly Motorola) and Texas Instruments. Crouch holds 15 patents.

John Potter, formerly the principal automation architect at Inovys Corp. and an engineering supervisor at Motorola Corporation, joined ASSET's IJTAG tools development effort as senior principal technologist, core instrumentation. Potter is a recent member of the P1687 IJTAG working group focusing on language. He holds two patents.

For more information, visit www.asset-intertech.com.



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