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## Products: Test/Measurement

### Design validation/test tool supports Intel IBIST

[Gina Roos](#)

[eeProductCenter](#)

11/01/2007 10:32 PM

Richardson, Tex. ASSET InterTech has expanded its ScanWorks design validation, test and debug tool by adding signal integrity analysis applications that support Intel's Interconnect Built-In Self Test (IBIST) embedded design validation and test technology.

ASSET's ScanWorks environment with support for IBIST consists of software applications for validating and analyzing signal integrity on buses through margining and BERT techniques. ScanWorks interfaces to embedded instruments to validate board-level and system-level designs that feature high-speed buses such as PCI Express, Intel's QuickPath interconnect and others. ScanWorks also features a

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For those of you involved in embedded systems development; which of the following types of operating system are you planning to use in your next project:

- In-house developed OS
- Commercial proprietary OS
- Linux
- My project doesn't need an OS

suite of fault diagnostic tools.

IBIST technology is a design validation and test architecture embedded into many of Intel's processors to enable chip-to-chip interconnect testing and design validation of high-speed buses on a printed-circuit-board. IBIST leverages the boundary-scan IEEE 1149.1 specification as the hardware and software communication methodology for accessing and controlling its embedded on-chip capabilities.

Product information: [ScanWorks Intel IBIST tools](#)

**ASSET InterTech, Inc.**, 1-888-694-6250, [www.asset-intertech.com](http://www.asset-intertech.com)

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**Around EE Times Europe****NXP shifting chip design to India**

Development of several product lines at NXP Semiconductors' home electronics division unit are being moved out of Europe, mostly to Asian countries, according to Christos Lagomichos, executive vice president and general manager of the division.

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The European Union hopes that its proposed blue-card program will provide a more attractive alternative to the U.S. green-card program, which critics say is plagued by backlogs, cumbersome processes, and insufficient quotas.



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Trevor Darnborough, whose company, Darnbro, filed for a patent on securing RFID tags to clothing, hopes other schools will be interested.



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Catrene - the Cluster for Application and Technology Research in Europe on NanoElectronics - is to be the \$8.5 billion European collaborative research program to take electronics into the nanoscale era.



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The eBay subsidiary will work with U.K.-based 3 Mobile to release the "white phone" later this month in four countries.



### [Shakeup hits Q3 chip vendor ranking](#)

Intel and Samsung remained the world's first and second largest IC makers in Q3 but Toshiba moved past Texas Instruments and STMicroelectronics to take third spot, according to IC Insights.



### [Silicon 60 version 6.1](#)

The EE Times 60 Emerging Startups list, first published in April 2004, has been updated to version 6.1 to reflect the latest corporate, commercial, technology and market conditions. Readers are encouraged to nominate startups for the next iteration of the list.

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